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L2	6	aircraft adj engine adj model	USPAT	OR	OFF	2005/03/04 14:22
L3	23	("4215412" "4325123" "4785403" "4837695").PN. OR ("5070458").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/03/04 14:34
L4	3860	((701/14) or (701/2) or (707/102) or (703/2) or (703/7) or (703/8)). CCLS.	USPAT	OR	OFF	2005/03/04 14:35
L5	124	4 and model and regression	USPAT	OR	OFF	2005/03/04 14:35
L6	12	5 and (data near format)	USPAT	OR	OFF	2005/03/04 14:39
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Alessandro Bogliolo, Luca Benini, Giovanni De Micheli

July 2000 **ACM Transactions on Design Automation of Electronic Systems (TODAES)**,
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Register-transfer level (RTL) power estimation is a key feature for synthesis-based design flows. The main challenge in establishing a sound RTL power estimation methodology is the construction of accurate, yet efficient, models of the power dissipation of functional macros. Such models should be automatically built, and should produce reliable average power estimates. In this paper we propose a general methodology for building and tuning RTL power models. We address both hard macros (presy ...

Keywords: RTL design, RTL power modeling, adaptive characterization, functional macros, regression models

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Michael A. Duffy

March 1978 **Proceedings of the 11th annual symposium on Simulation**

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Justin Picard

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





















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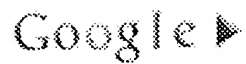
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